

CIRCUIT AND METHOD FOR ALIGNING DATA TRANSMITTING TIMING OF A PLURALITY OF LANES

Abstract

A circuit and a method for aligning data transmitting timing of a plurality of lanes. The method includes utilizing different initial values to reset a count value corresponding to a lane when a plurality of COM symbols are detected on the lane, utilizing an increment value to increase the count value corresponding to the lane when a COM symbol is not detected on the lane, and utilizing a plurality of count values corresponding to the lanes to align data transmitting timing of the lanes when a COM symbol is not detected on the lanes within a predetermined period of time.